

## ABSTRACT OF THE DISCLOSURE

5 A semiconductor integrated circuit having: an internal main bus;  
first microprocessor and second microprocessor sharing the internal main  
bus; a first debug serial bus with one end thereof connected to said first  
microprocessor; a second debug serial bus with one end thereof connected to  
the second microprocessor; and a debugging module connected to the other  
ends of the first debug serial bus and second debug serial bus and  
transferring at least a debugging program and debugging data to the first  
10 microprocessor via said first debug serial bus and to the second  
microprocessor via the second debug serial bus.

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